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cancel.

a contact hole 44 to the pixel electrode 28 at a drain electrode part 42A. Accordingly, the contact hole for connecting the pixel electrode 28 to storage electrode part 42B is eliminated to reduce an amount of overlap between the storage electrode part 42B and the pixel electrode 28. Note also in Fig. 3 that pixel electrode 28 overlaps gate line 24, the edge of pixel electrode 28 designated by reference 28A, and contrast FIG. 2 in which pixel edge 28A overlaps the gate line more than in Fig. 3. The contact hole for connecting the pixel electrode 28 to storage electrode part 42B is eliminated in Fig. 3. --

IN THE CLAIMS

Please amend the claims as follows:

B2

9. (Twice Amended) The liquid crystal device of claim 8, wherein the drain electrode part has a greater area than a drain electrode part electrically connected to the pixel electrode via a contact hole in the protective layer over the drain electrode part.

B3

18. (Twice Amended) The liquid crystal device of claim 17, wherein the pixel electrode overlaps a gate line, defining the cell but not connected to the thin film transistor, the overlap of the gate line being less than an overlap in a case wherein the protective layer includes a contact hole over a storage electrode part of the metallic pattern.

21. (Twice Amended) A liquid crystal device having a thin film transistor, comprising:

a plurality of gate lines formed on a substrate;

BY a plurality of data lines insulated from and intersecting said gate lines, said data lines and intersecting gate lines defining a plurality of cells, at least one cell including,

a pixel electrode,

ad C 2 a thin film transistor, interposed between one of the data lines and the pixel electrode and including a source electrode connected to the one of the data lines, a gate electrode connected to one of the gate lines a drain electrode, and

a storage capacitor having a storage electrode and a drain electrode, the storage capacitor being connected to the pixel electrode.

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